Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

13477

Application ID:

09683486

FIN-BASED DOUBLE POLY

DYNAMIC THRESHOLD CMOS FET

Title of Invention:

WITH SPACER GATE AND

METHOD OF FABRICATION

First Named Inventor:

Andres Bryant

Domestic/Foreign Application:

Domestic Application

Filing Date:

null

Effective Receipt Date:

2002-01-07

Submission Type:

Utility Patent Filing

Filing Type:

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new-utility

Confirmation Number:

0

Attorney Docket Number:

BUR920010086

cn=Mark F. Chadurjian, ou=Registered Attorneys, ou=Patent and

Digital Certificate Holder:

Trademark Office, ou=Department of Commerce, o=U.S.

Government, c=US

Certificate Message Digest:

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Total Fees Authorized:

\$870.0

Payment Category:

DA - Deposit Account

Deposit Account Number:

90456

Deposit Account Name:

Mark F. Chadurjian

TRANSMITTAL FORM



Electronic Version 1.0.3

Stylesheet Version: 1.0

Submission Type: Utility

Patent Filing

Attorney Docket Number:

BUR920010086

FIN-BASED DOUBLE POLY DYNAMIC THRESHOLD CMOS FET WITH SPACER GATE AND METHOD OF FABRICATION

First Named Inventor: Andres Bryant

SUBMITTED BY

Name:

the first when the first that the first that

Mr. Mark F. Chadurjian

Registration Number:

30,739

Electronic Signature Mark: Mark F.

Chadurjian / s /

Date Signed: 20000103

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Attached Files:

declaration

bur920010086dec1.tif

declaration

bur920010086dec2.tif

declaration
specification
bibd-transmittal
patent-assignments
fee-transmittal

Attached Image File(s): bur920010086dec1.tif bur920010086dec2.tif bur920010086dec3.tif bur920010086dec3.tif bur920010086.xml bur920010086apds.xml bur920010086asgn.xml bur920010086fee.xml Comments:

Express Mailing Label No.: ET101904328US

(JOINT INVENTORS)

Atty. Docket No.: BUR920010086US1

Declaration and Power of Attorney for Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

FIN-BASED DOUBLE POLY DYNAMIC THRESHOLD CMOS FET WITH SPACER GATE AND METHOD OF FABRICATION

the specific	ation of which (check one)			
х	is attached hereto.			
	was filed on	as Applicatio	on Serial No.	and was
	amended on	-		
	ate that I have reviewed and le claims, as amended by any			d specification,
	dge the duty to disclose infore with Title 37, Code of Feder		I to the patentability of thi	s application in
foreign app foreign app	aim foreign priority benefits u olication(s) for patent or inve olication for patent or inventor ity is claimed:	ntor's certificate listed be	elow and have also ident	ified below any
	or Foreign Application(s): mber	Country	Day/Month/Year	Priority Claime
I hereby cla below .	aim the benefit under Title 35, I	United States Code, §120	of any United States app	lication(s) listed
Ap	plication Number		Filing Date	
below and, United Sta	aim the benefit under Title 35, insofar as the subject matter tes application in the manner knowledge the duty to disclo Title	of each of the claims of the provided by the first pa	his application is not disck ragraph of Title 35, Unite	osed in the prior ed States Code,

Status

Prior U.S. Applications:

	• •	
Serial No.		Filing Date

the national or PCT international filing date of this application:

I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and

Mark F. Chadurjian	Richard A. Henkler	Richard M. Kotulak	James M. Leas
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Page 2 of 3

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(JOINT INVENTORS)

Any. Docket No.: BUR920010086US1

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

(1)inventor Andres Boyar Signature: 19 Wilkinson Drive Residence: Essex Junction, VT 05452 Citizenship: United States of America Post Office Address: Same as Residence (2)inventor: K. Paul Müller Signature: Residence: 89 Brothers Road Wappingers Falls, NY 12590 Citizenship: Germany Post Office Address: Same as Residence (3)inventor: Edward J. Nowak Signature: Residence: 8 Windridge Road Essex Junction, VT 05452 Citizenship: United States of America **Post Office**

Page 3 of 3

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FEE TRANSMITTAL

Electronic Version 1.1.0 Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Large Entity

TOTAL FEES AUTHORIZED: \$870

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

Deposit Account Number: 09-0456

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Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

SUBMITTED BY

Authorized Name:

Mark F. Chadurjian

Electronic Signature Mark:

Mark F. Chadurjian / s /

Date Signed:

20020103

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid		
Utility Filing Fee	101	\$ 740		

Subtotal For Basic Filing Fee: \$ 740

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 25	103	\$ 18	5	\$ 90
Independent Claims: 3	102	\$ 84	0	\$ 0

ADDITIONAL FEES

Fee Description	Number	Quantity	Fee Code	Amount	Fee Paid
Recording Each Patent Assignment Per Property Fee	00000000	1	581	\$ 40	\$ 40

Subtotal For Additional Fees: \$ 40